

Fig. 1a

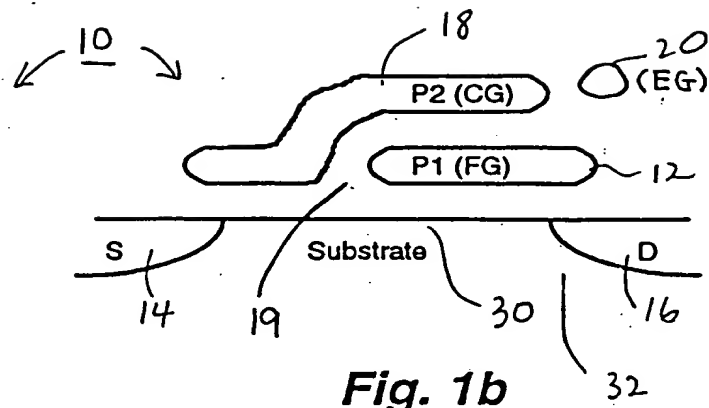


Fig. 1b

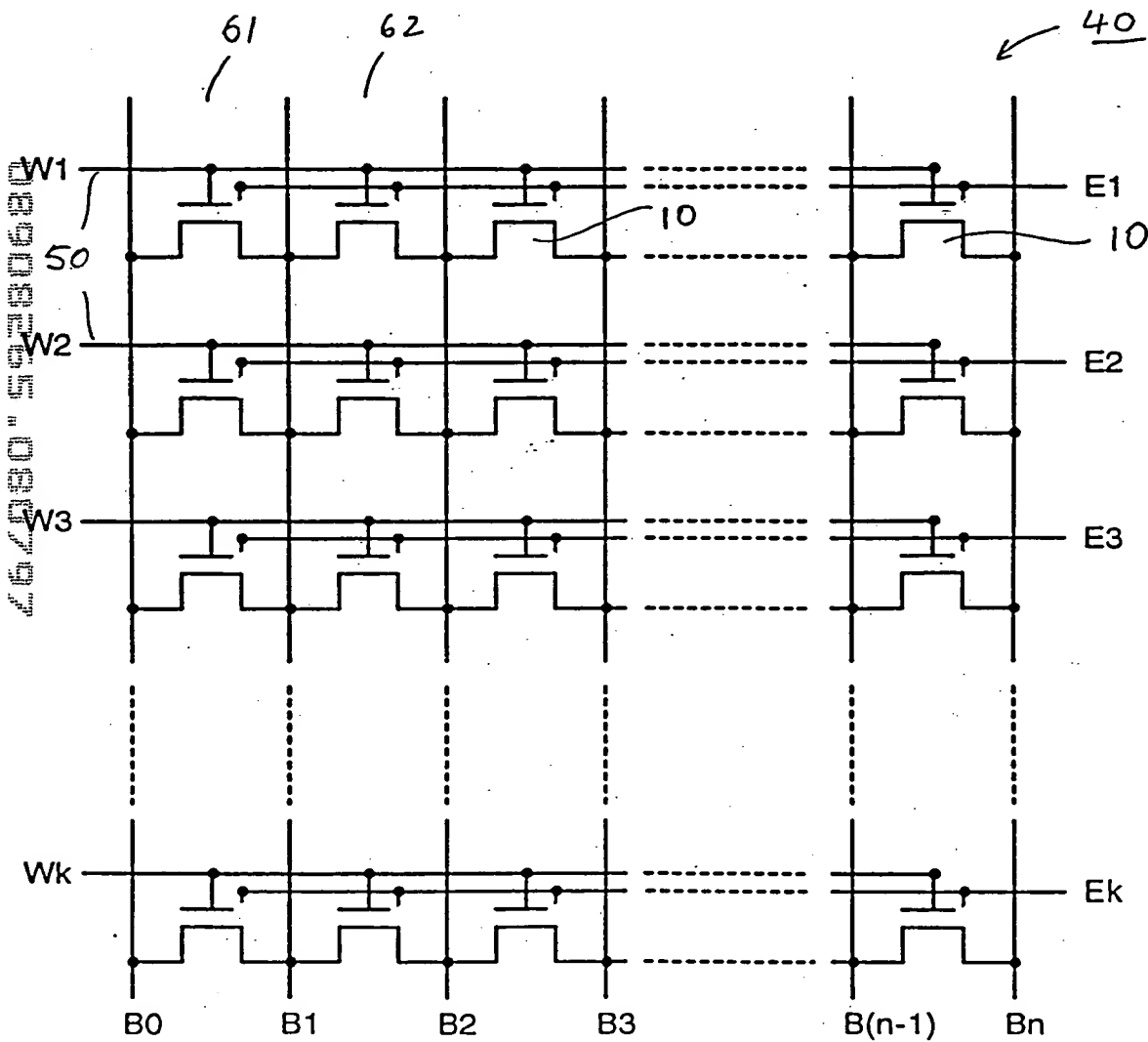


Fig. 2

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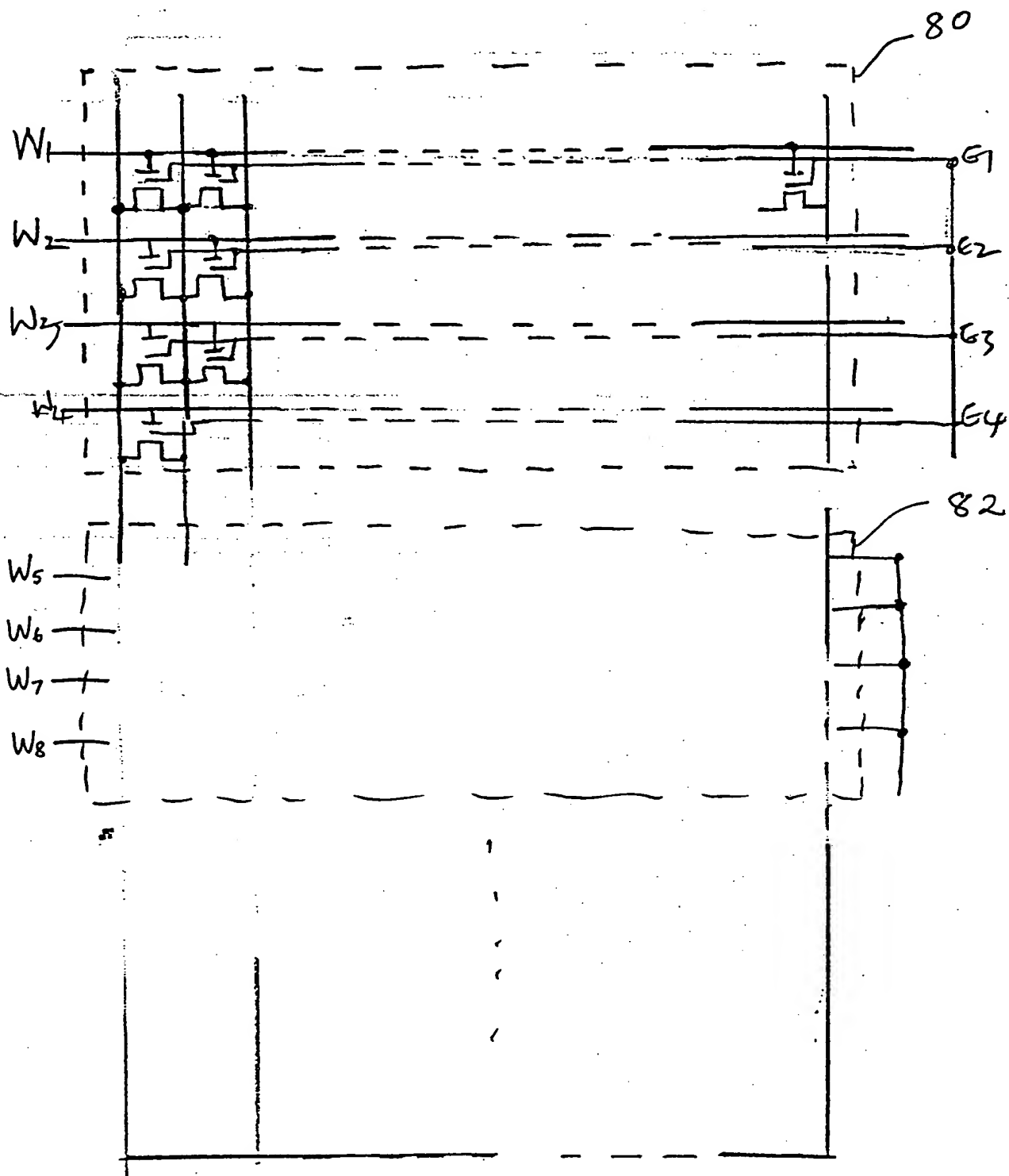


FIG. 2b

# VOLTAGE CONDITIONS FOR NORMAL MEMORY OPERATIONS

TYPE OF LINES	READ	PROGRAM	ERASE
Selected Word line (WL)	5V	12V	0V
Non-selected Word line (WL)	0V	0V	0V
Selected Source line (SL)	0V	0V	0V
Selected Drain line (DL)	1.5V	8V	0V
Non-selected bit lines (BL)	float	float	0V
Selected Erase line (EL)	0V	0V	20V
Non-selected Erase line (EL)	0V	0V	0V

FIG. 3

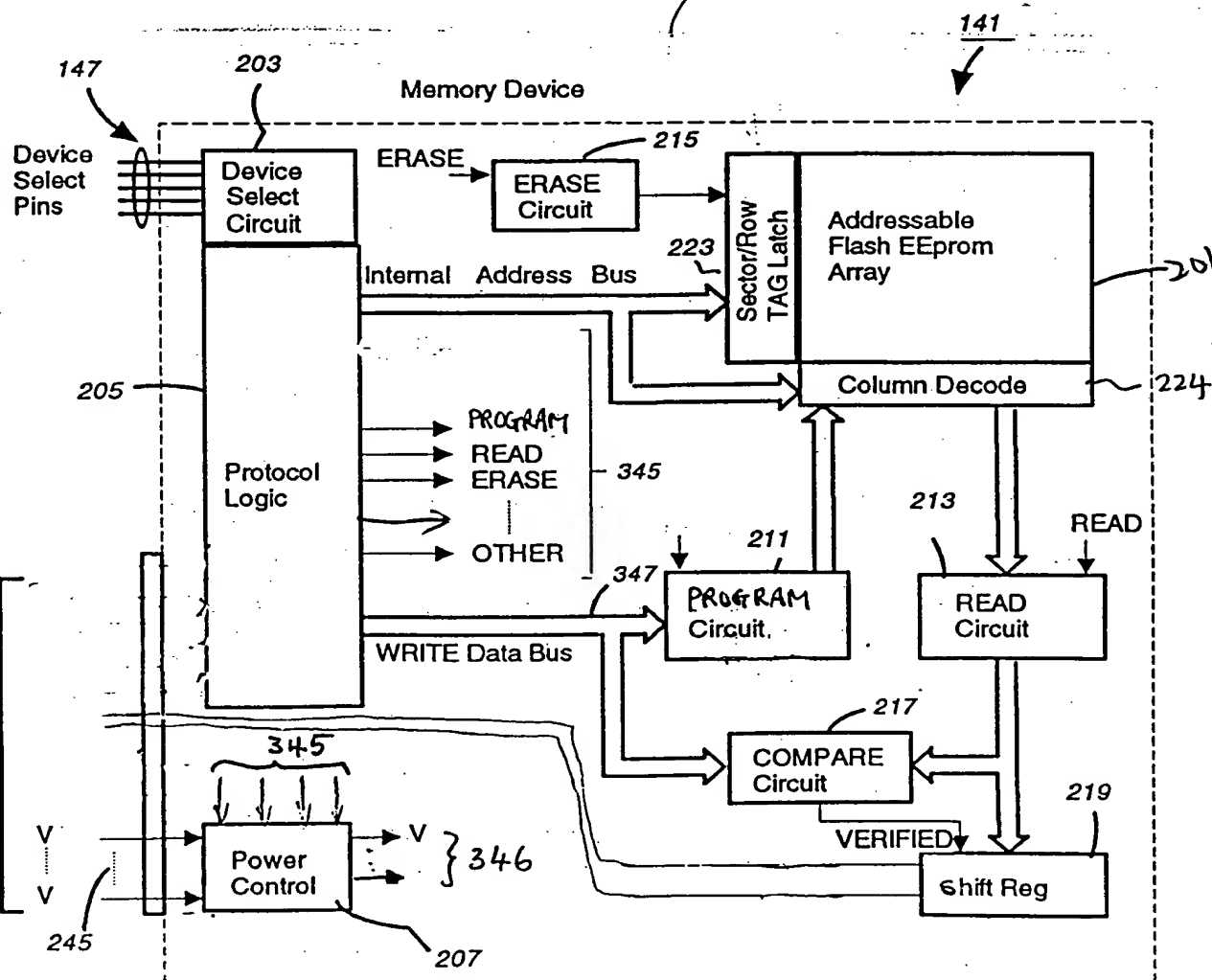


Fig. 4

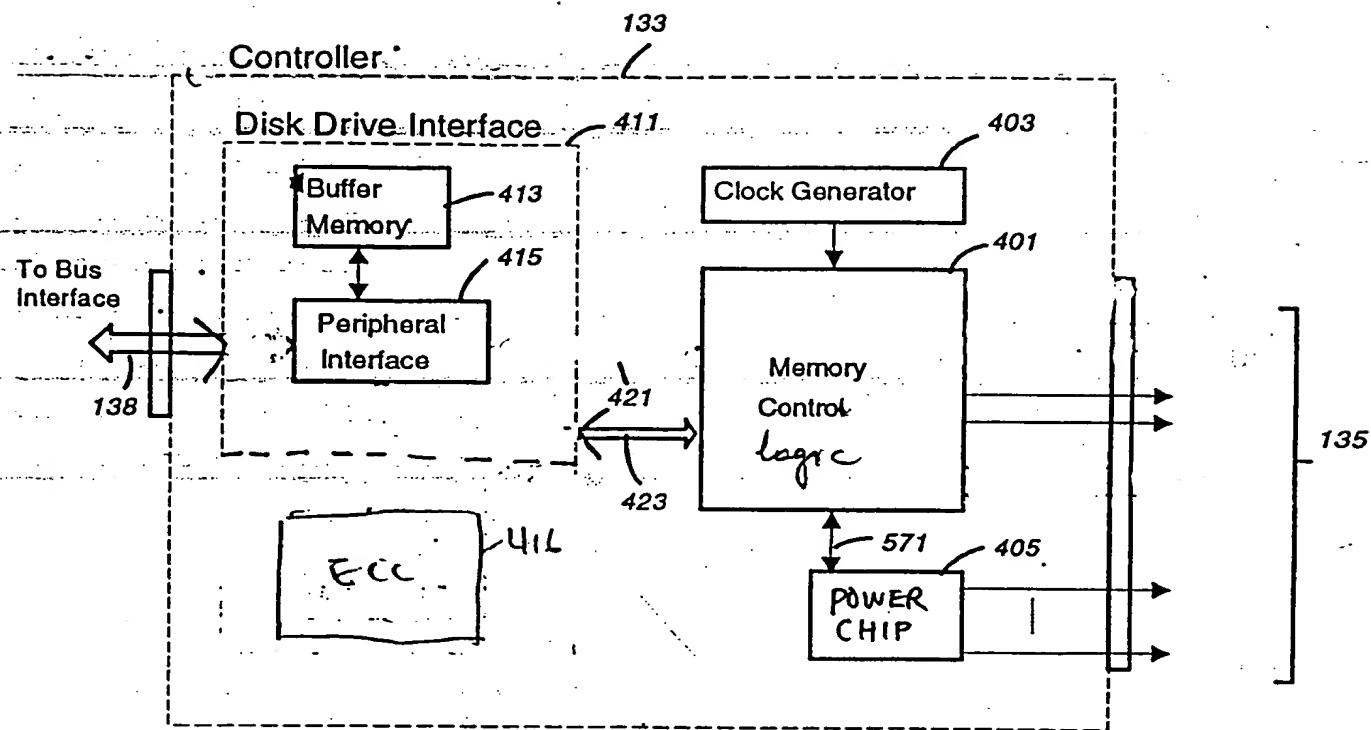
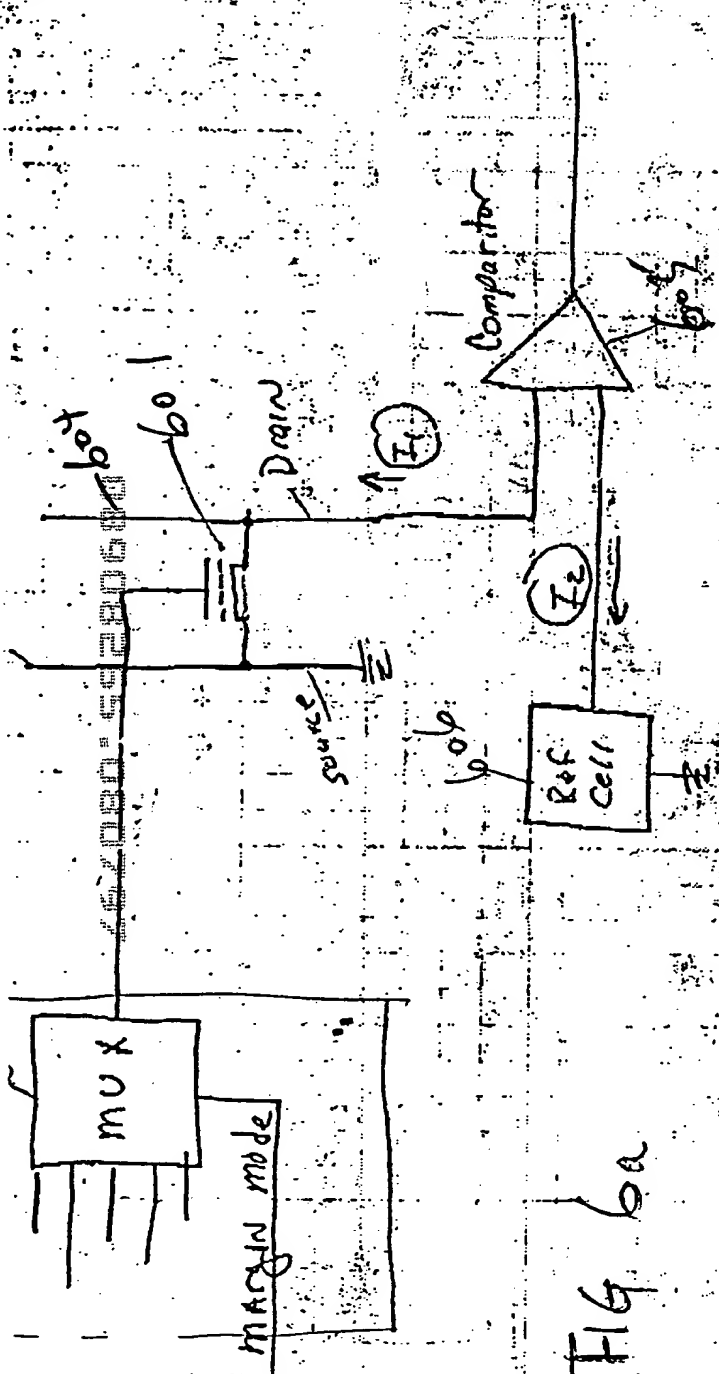


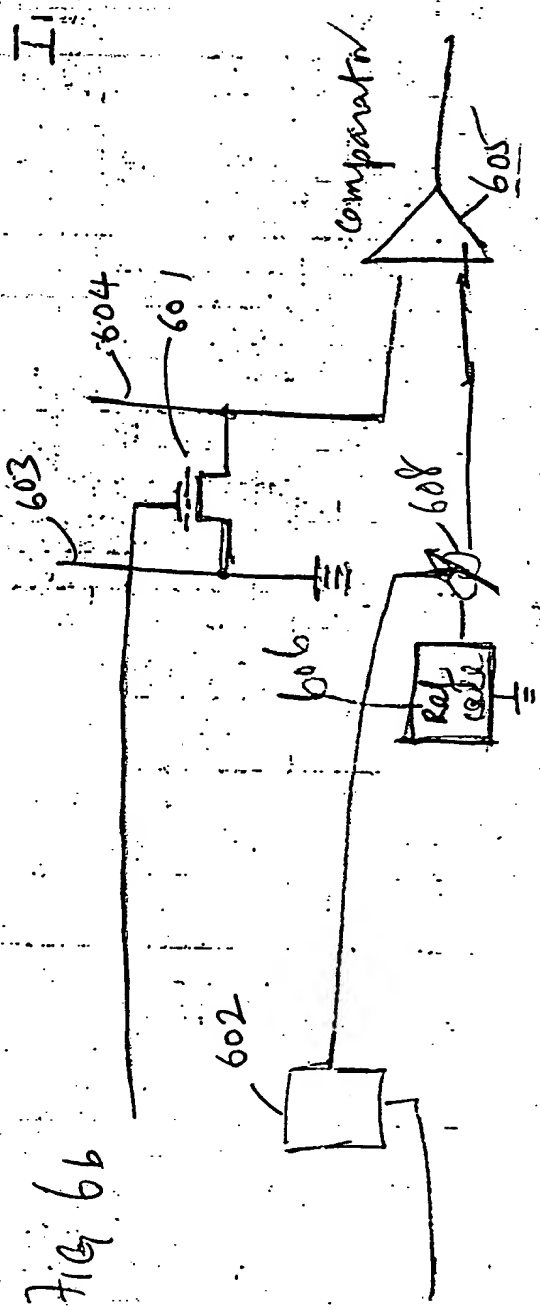
Fig. 5

00908265 00079 464080 59280680



$$I_1 > I_2 \Rightarrow \Phi$$

$$I_1 < I_2 \Rightarrow 1$$





662080-59280680

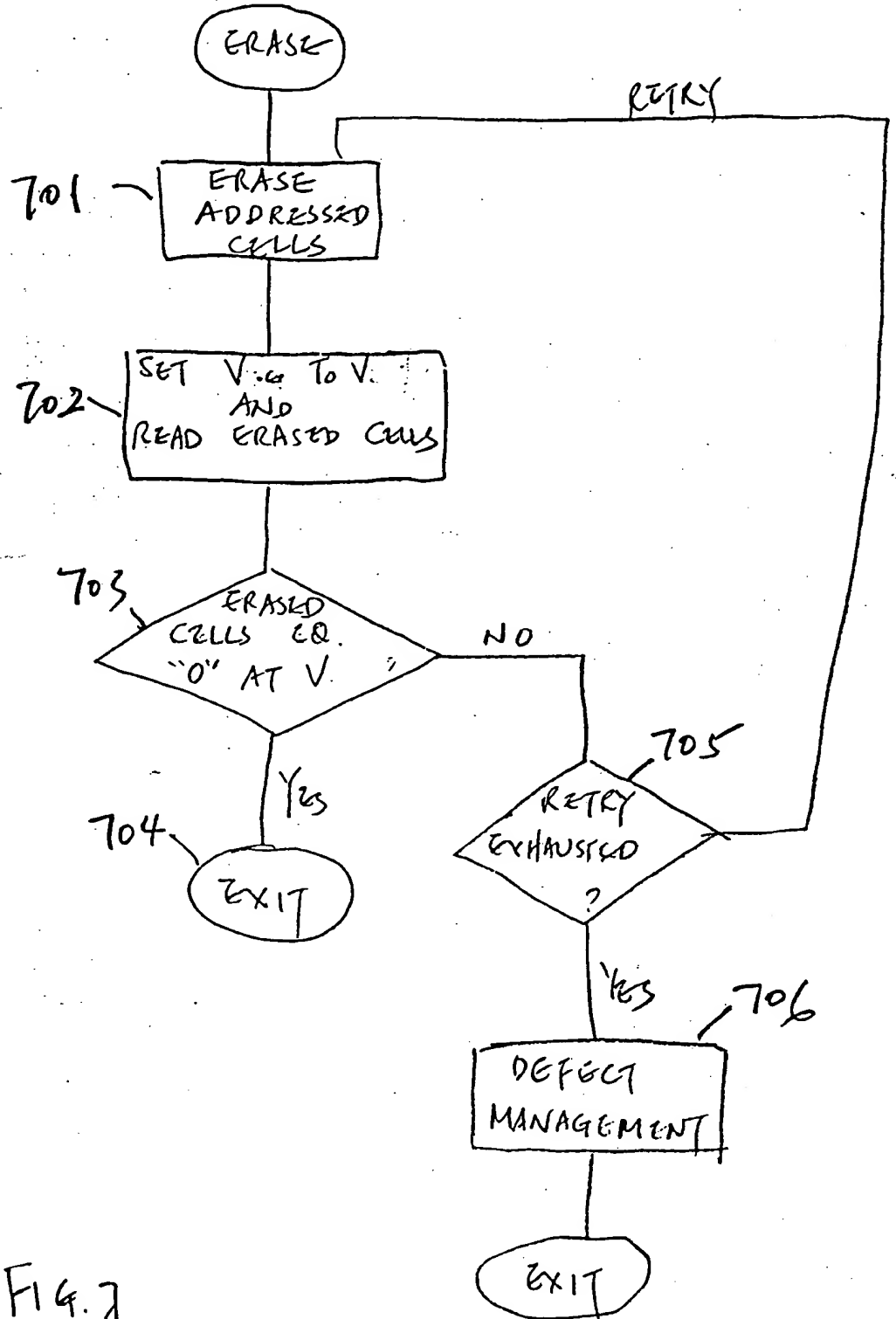


FIG. 7

152080" 59280680

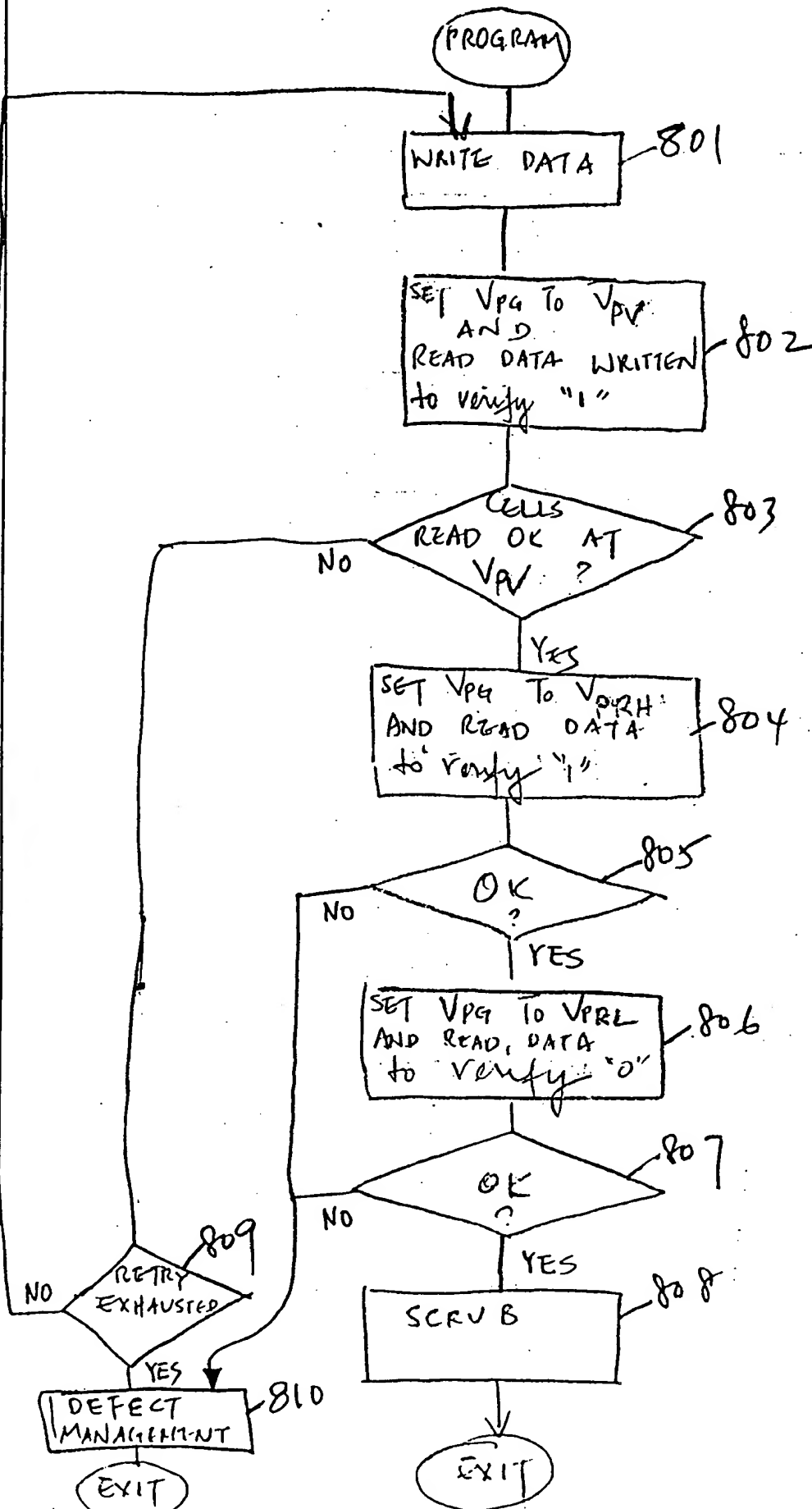


FIG. 8

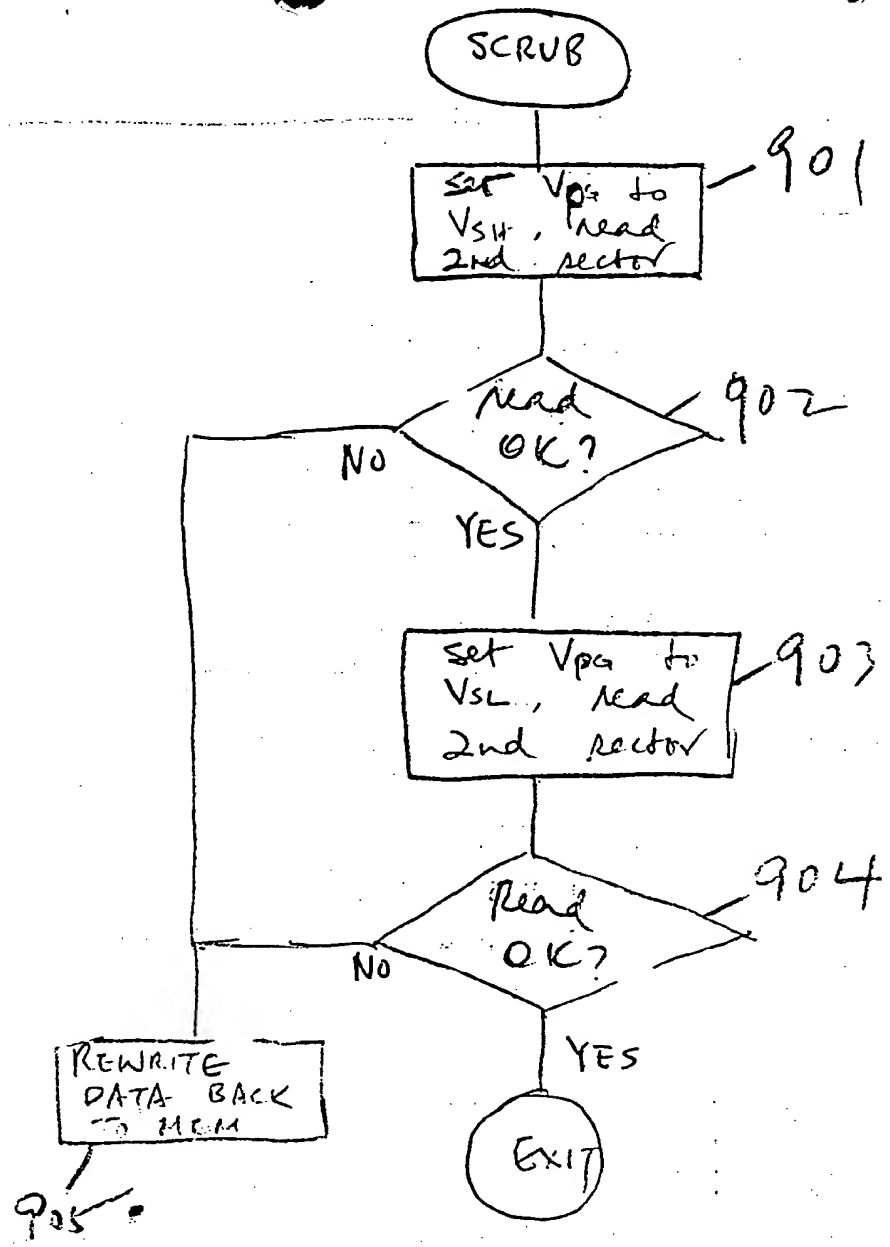


FIG. 9



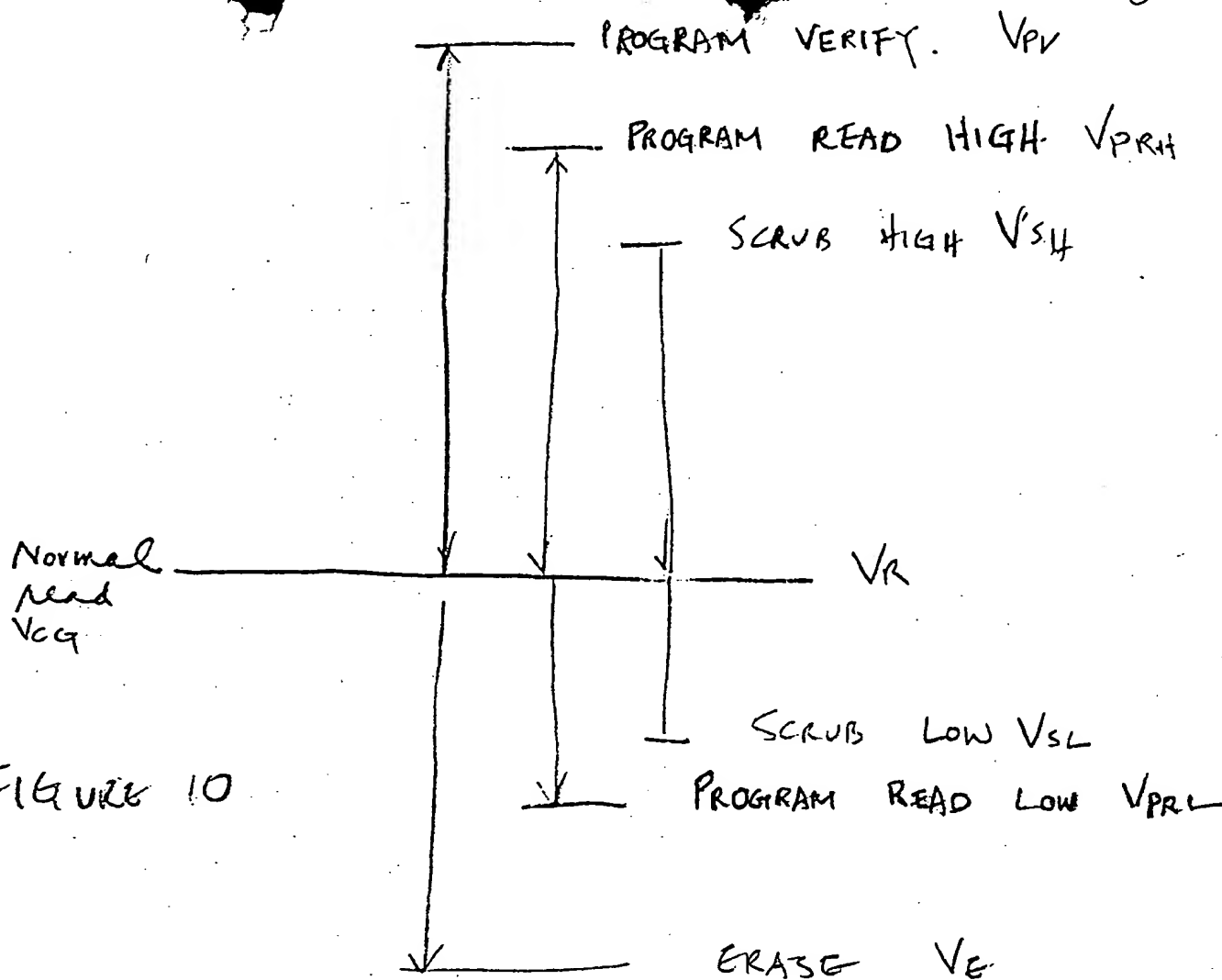


FIGURE 10

$V_{pv}$	6.4
$V_{prh}$	5.6
$V_{sh}$	5.2
$V_R$	5.0
$V_{sl}$	4.4
$V_{prl}$	4.2
$V_e$	3.8

FIGURE 11

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